

User-Propagators for Custom Theories in SMT Solving

– In a Nutshell –

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Research

Recap

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int32 i1 , i2 ;  
...  
assume(i1 > 0);  
arr[0] = 1;  
arr[i1 + i2] = 2;  
assert(arr[0] = 1);
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int32 i1 , i2 ;           ... ^
...                       i1 > 0 ^
assume(i1 > 0);           ⇒ arr1 = store(arr0, 0, 1) ^
arr[0] = 1;               arr2 = store(arr1, i1 + i2, 2) ^
arr[i1 + i2] = 2;        select(arr2, 0) ≠ 1
assert(arr[0] = 1);
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<code>int32 i1, i2;</code>	<code>... ^</code>	
<code>...</code>	<code>i1 > 0 ^</code>	$array_0 \mapsto \langle 0, \dots, 0 \rangle,$
<code>assume(i1 > 0);</code>	$\Rightarrow arr_1 = store(arr_0, 0, 1) \wedge$	$array_1 \mapsto \langle 1, \dots, 0 \rangle,$
<code>arr[0] = 1;</code>	$arr_2 = store(arr_1, i1 + i2, 2) \wedge$	$array_2 \mapsto \langle 2, \dots, 0 \rangle,$
<code>arr[i1 + i2] = 2;</code>	$select(arr_2, 0) \neq 1$	$i1 \mapsto 2^{31},$
<code>assert(arr[0] = 1);</code>		$i2 \mapsto 2^{31}$

Relevancy

My talk might be of *special interest* for you if . . .

- ▶ You need some "crazy" theory that is not directly supported by your SMT solver
- ▶ You encounter bad performance on solving some big problem in SMT
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Note: This talk should only convey the general idea; just relax and check if you might utilize the user-propagator for your own needs. We are looking for use-cases and can add new features if required. I am happy to discuss things in more detail individually after the talk :-)!

Topics

More precisely, we can use user-propagation for (incomplete list):

- ▶ Defining *custom theories* (finite or infinite)
 - ▶ e.g., graphs, posets, (new) strings
- ▶ *Lazily instantiating* potentially unused axioms (less required memory - more speed)
 - ▶ e.g., We will see a simple example
- ▶ Easy and high performance *constraint satisfaction problem* solving
 - ▶ e.g., Encoding the n -queens problem (vastly increases performance/decreases memory)
- ▶ Implementing custom variable selection/assignment *heuristics*
- ▶ Efficiently/Easily expressing *aggregates and scheduling constraints*
 - ▶ e.g.,
$$3 \cdot |\{(i, j) \mid i < j \wedge x_i + x_j = 42 \wedge (x_i > 30 \vee x_j > 30)\}| + |\{(i, j) \mid i < j \wedge x_i + x_j = 42 \wedge \neg(x_i > 30 \vee x_j > 30)\}|$$
- ▶ Speeding-up propositional *AllSAT*
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- ▶ Extend Z3 with *additional semantics* (partially)
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- ▶ *Logging/Debugging* (potentially replaying proofs – let's see) intern Z3 actions
- ▶ ...
- ▶ *Educating* SMT-Solving
- ▶ *Drafting* new decision procedures in a rather easy "framework"

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3. Attach callbacks (= functions) to desired events.

{ *Fixed* = Some-Code-In-C++, *Eq* = Or-Some-Code-In-Python, ... }

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5. The callbacks may interfere if required.

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A:

- ▶ Adding conflicts:

e.g., "You (the solver) assigned $\langle z \mapsto 3, x \mapsto 7, 'c - 5 = x' \mapsto \perp \rangle$ but I don't want that. Please backtrack!"

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A:

- ▶ Adding conflicts
- ▶ Adding further constraints ("propagating lemmas"):
e.g., "(Some parts of) Your current assignments enforces, that z must be greater than c . Please keep that in mind."

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A:

- ▶ Adding conflicts
- ▶ Adding further constraints ("propagating lemmas")
- ▶ Interfere in variable selection heuristics:
e.g., "If you cannot derive anything any more without guessing then try ' $c - 5 = x$ ' $\mapsto \perp$ next."

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- ▶ Further: push, pop, fresh, decide, diseq, and created

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Nice, however it is called Alpine **Verification** Meeting, *not* Alpine **Constraint Satisfaction** Meeting ...

However, ...

consider the variant: We have disjoint intervals $[addr_i, addr_i + size_i]$:

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This is an axiom in the verification tool alive2 to ensure that allocated memory blocks are disjoint.

- ▶ Quadratic memory requirement.
- ▶ Axiom probably not violated \Rightarrow Solver should not focus on it.
- ▶ Time consuming to generate the axiom.

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We transform it to $A \wedge \forall addr_1, \dots, size_n : (disjoint(addr_1, \dots, size_n) \Rightarrow B)$

and observe also the truth value of $disjoint(\dots)$ and distinguish:

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- ▶ Else we are fine

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Questions?

