# Commutativity in Concurrent Program Verification 

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AVM 2022

## Example Program

$$
\{x=y=i=j=0\}
$$



## Naïve Sequentialization



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$$
x=\sum_{k=0}^{i} A[k] \wedge y=\sum_{k=0}^{j} A[k] \wedge i \leq n \wedge j \leq n
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## Commutativity-Based Equivalence

Many statements commute: execution order does not matter

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\text { Example: } \mathrm{x}+=\mathrm{A}[\mathrm{i}] \quad \mathrm{y}+=\mathrm{A}[\mathrm{j}] \sim \mathrm{y}+=\mathrm{A}[\mathrm{j}] \quad \mathrm{x}+=\mathrm{A}[\mathrm{i}]
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Extension: proof-sensitive commutativity
Example: $\mathrm{B}[\mathrm{k}]:=\mathrm{c}$ commutes with $\mathrm{B}[1]:=\mathrm{d}$ if proof guarantees $k \neq l \vee c=d$

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Key Property: Correct traces only equivalent to correct traces.

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## Reduction

One representative trace for each equivalence class

## Naïve Sequentialization



Reduction I


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Iteratively construct Floyd/Hoare-style proof of program

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[1] Ebbinghaus. Tight Integration of Partial Order Reduction into Trace Abstraction Refinement. BSc Thesis [2] Klumpp, Dietsch, Heizmann, Schüssele, Ebbinghaus, Farzan and Podelski. Ultimate GemCutter and the Axes of Generalization - (Competition Contribution). TACAS 2022


## Evaluation

Implemented in Ultimate GemCutter software model checker Evaluated on SV-COMP'21 benchmarks and benchmarks of WEAVER tool

analyzed 50 more programs using significantly less time ( $\approx 50 \%$ ), memory ( $\approx 27 \%$ ), and refinement rounds ( $\approx 64 \%$ )

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## Preference Orders

## Selecting the right representatives

[3] Farzan, Klumpp and Podelski. Sound sequentialization for concurrent program verification. PLDI 2022

Reduction I


Reduction | $x=\sum_{k=0}^{i} A[k] \wedge i \leq n \wedge y=0 \wedge j=0$

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-\sum_{i n}
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Reduction II


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- independent of commutativity
- same scheme of preference order applies to different programs


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Algorithmic construction of reductions using techniques from partial order reduction:

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- linear-size representation in the best case
$\rightarrow$ More on preference orders in Marcel's talk


# Commutativity Relations 

## at different abstraction levels

[work in progress; presented at Commute workshop @ PLDI'22]

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## Safe Commutativity

Let $\Pi$ be a proof (a set of Hoare triples).

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- commutativity $I_{C}$ based on (concrete) semantics: safe wrt. all proofs $\Pi$
- How to get safe commutativity for a particular proof $\Pi$ ?


## Safe Abstraction

Let $\alpha:$ Stmt $\rightarrow$ Stmt .

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I_{\alpha}:=\left\{\left(s t_{1}, s t_{2}\right) \mid \llbracket \alpha\left(s t_{1}\right) \alpha\left(s t_{2}\right) \rrbracket=\llbracket \alpha\left(s t_{2}\right) \alpha\left(s t_{1}\right) \rrbracket\right\}
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## Theorem (Safety)

If $\alpha$ satisfies

- abstraction: $\llbracket s t \rrbracket \subseteq \llbracket \alpha(s t) \rrbracket$ for all st
- preservation: $\{\varphi\} \alpha(s t)\{\psi\}$ is valid, for all $\{\varphi\} s t\{\psi\} \in \Pi$
then $I_{\alpha}$ is safe wrt. П.


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& \alpha_{\Pi}(\mathrm{y}:=\mathrm{x}+\mathrm{x}): \\
& \alpha_{\Pi}(\mathrm{x}:=0) \text { : "assign y to some even value (nondet.)" } \\
& \text { "do not change } \mathrm{y} "
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Now: $\alpha_{\Pi}(\mathrm{y}:=\mathrm{x}+\mathrm{x})$ commutes with $\alpha_{\Pi}(\mathrm{x}:=0)$.

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```
x:=1 x==1;z:=1 x==2;z:=2
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x:=1 \quad x==1 ; z:=1 \quad x==2 ; z:=2 \quad \sim_{I_{C}} \quad x:=1 \quad x==2 ; z:=2 \quad x==1 ; z:=1
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& \sim_{I_{\alpha}} \mathrm{x}==2 ; \mathrm{z}:=2 \quad \mathrm{x}:=1 \quad \mathrm{x}==1 ; \mathrm{z}:=1
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|  | (1) abstract | (2) concrete |  |  |
| :---: | :---: | :---: | :---: | :---: |
| $\tau_{1}$ | $\sim I_{\alpha}$ | $\tau_{2}$ | $\sim I_{C}$ | $\tau_{3}$ |
|  |  | $\Rightarrow$ |  | $\stackrel{\uparrow}{\text { orrect }}$ |

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Combination through new proof rule:

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New partial order reduction algorithm for $n$ commutativity relations

## Conclusion

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## Questions?

